

<b>Notice of References Cited</b>	Application/Control No. 10/696,146	Applicant(s)/Patent Under Reexamination GALLES ET AL.	
	Examiner William M. Treat	Art Unit 2181	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
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	C	US-			
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**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Kenneth Yeager, "The MIPS R10000 Superscalar Microprocessor", IEEE Micro, vol. 16, iss. 2, April 1996, pp. 28-41.
	V	Agarwal et al., "An Evaluation of Directory Schemes for Cache Coherence", Proceedings of the 15 <sup>th</sup> International Symposium on Computer Architecture, IEEE, May 30 - June 2, 1988, pp. 280-289.
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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